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ſ	APPLICATION NO. FILING DATE			FIRST NAMED	INVEN	TOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
7	09/837,882 04/18/2001 Christopher H. Pham CISOTTSUS 8340 TITLE OF INVENTION: LINEAR ASSOCIATIVE MEMORY-BASED HARDWARE ARCHITECTURE FOR FAULT-TOLERANT ASIC/FPGA WORK-AROUND								
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L	APPLN. TYPE	SMALL ENTITY	ISSUE F		. PU	JBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE	
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3	ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type) PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment. (A) NAME OF ASSIGNEE (B) RESIDENCE: (CITY and STATE OR COUNTRY) SAN JOSE, CALIFORNIA								
_	Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government								
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	5. Change in Entity Status (from status indicated above) \[\sum_{a.} Applicant claims SMALL ENTITY status. See 37 CFR 1.27. \] \[\sum_{b.} Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).								
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